SLM755(SLM755LC) Q ‡?ô lB$ > _V1.02

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1. × ñ ²F

SLM755(SLM755LC) _ 0!n * ¾Q FJ MSM8909 £ Ã J4×Q W7- Ã F >| ¹ ƒ ý Œ
3+5 Ã ¯ ‘ µ ˆ j 8GB+1GB(¬ é 16GB+2GB),´ ˜ f ? J Q LTE ª7-FJ Q ‡ È _ 1
TD-LTE/FDD-LTE/WCDMA/TD-SCDMA/EVDO/CDMA/GSM J/ý 5 • 5 f ? È _ 1
GPS/Glonass/Beidou J/ý f ? › O Ê } È _ 1 J/ýB N# úN#NÁ5F@ .1 È3+5 Lö @ ¶
Adreno 304 ,´ Q W7-GPU È _ 1 720P ,´?öNÁ Ý n È 9 ` ü,´?öNÁ ÃN#NÁ Õ
ú ` ü,´ GPIO ™ ..ɪO Ä

SLM755(SLM755LC) _ 1,´ Õ •FO)· Ö
TDD-LTE: 117/30Mbps
FDD-LTE: 150Mbps/50Mbps
WCDMA Eî DC HSPA+: 42Mbps/5.76Mbps
EVDO Eî EVDO RevA: 3.1Mbps/1.8Mbps
TD-SCDMA Eî HSPA: 4.2Mbps/2.2Mbps
CDMA1x: 153.6kbps/153.6kbps
GSM Eî EDGE: 236.8kbps/2≠MW 1(6kb)1s

/S1MS(4.2LM7(4.255L)11(Tml)1)7(s)TE ETEMC /P ≠ 51D 85 BDC BT/C2 0 10.56 Tf1 019 1 954541 532.5 D58 11

2．(© W G>˜

× ñ(© W	ÿF
£	Qualcomm MSM8909
CPU	Quad-core A7 (32bit) 1.1GHz
GPU	A304 409.6MHz
3+5 µ ˆ	8GB eMMC + 1GB LPDDR3 ¬ é 16GB+2GB
ý Œ3+5	Android 5.1
j (	40.5x40.5x2.8mm

	Interface: MIPI DSI 4-lane	
Camera Ä } ¼ > ü t ÿ d Å	Interface: main: MIPI CSI0 2-lanes; front: MIPI CSI1 1-lanes	
	t ÿ d Pixel: Max: }5ž 5M/ >5ž 8M	
	Video decode	1080p 30 fps: HEVC/H264/ MP4/DivX/VP8 WVGA 30 fps:H.263
	Video encode	720p 30 fps:H264 WVGA 30 fps:VP8/MP4
EÃ •Aî 7	9K^ Ä 0 £ jK^ ÃN#Gÿ + ÃN#Gÿ Å	
	+e é ? TP	
Reset	_ 1.œ = }	
Ä+X Õ	Õ =0	k?± Ï7- ÿF
	VBAT	Q ‡+e$ÀEÃ • È3.3V 4.2V È 70 l 3.8V
	SDIO *1	TF Card È 0 W _ 1 32GB
	USB	_ 1 OTG FORCE_USB_BOOTÄ : ù j f USB E , È+X ¾3W U ;E- Å
	UART*2	04ô 4 4ï uart È 04ô 2 4ï uart
	I2C*4	For sensors/TP/others
	SPI*1	Master only
	ADC*2	_ 1
	u+e Ï7-	_ 1 FG u+e8ß(w
	PœEî	_ 1
	GPIO	24
	VCOIN	Î & &JÏ > 7+e"•
	4NÁ Õ	FÞ/˜ „
	Audio	2 D …1 MIC ÄECM&MEMS Å,1 D 6c j MIC 1 D } · Ä V Ï n Å 1 D \1,

3. SLM755(SLM755LC) Õ

36	UART1_CTS	B-PD:nppukp	Configurable I/O UARTor I2C SDA
37	UART1_RTS	B;B-PD:nppukp	Configurable I/O UARTor I2C SCL
38	VREG_L11_SDC	PO	PMIC output 2.95V
39	SDC2_SDCARD_CLK	BH-NP:pdpukp	Secure digital controller 2 clock
40	SDC2_SDCARD_CMD	BH-PD:nppukp	Secure digital controller 2 command
41	SDC2_SDCARD_D0	BH-PD:nppukp	Secure digital controller 2 data bit 0
42	SDC2_SDCARD_D1	BH-PD:nppukp	Secure digital controller 2 data bit 1
43	SDC2_SDCARD_D2	BH-PD:nppukp	Secure digital controller 2 data bit 2
44	SDC2_SDCARD_D3	BH-PD:nppukp	Secure digital controller 2 data bit 3
45	SDC2_SDCARD_DET	B-PD:nppukp	Configurable I/O ,SD_DET_N
46	FORCE_USB_BOOT	B-PD:nppukp	pullup to 1.8v will force MSM to boot from USB_HS port
47	TP_I2C_SCL	B;B-PD:nppukp	Configurable I/O CTP I2C
48	TP_I2C_SDA		

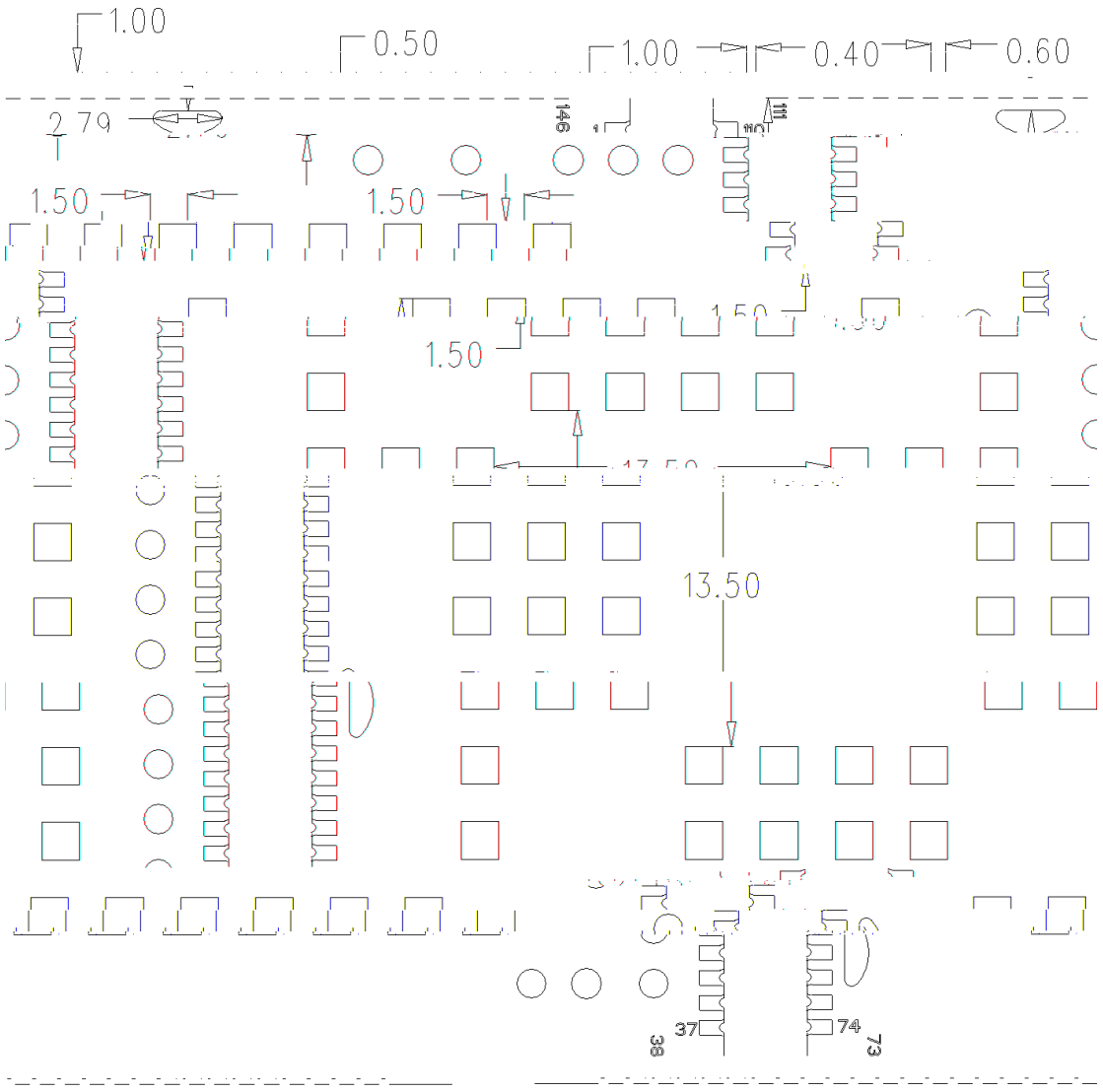
76	GND	GND

116	GPIO11_SPI_CLK	B-PD:nppukp	Configurable I/O SPI or I2C
117	GPIO10_SPI_CS	B	

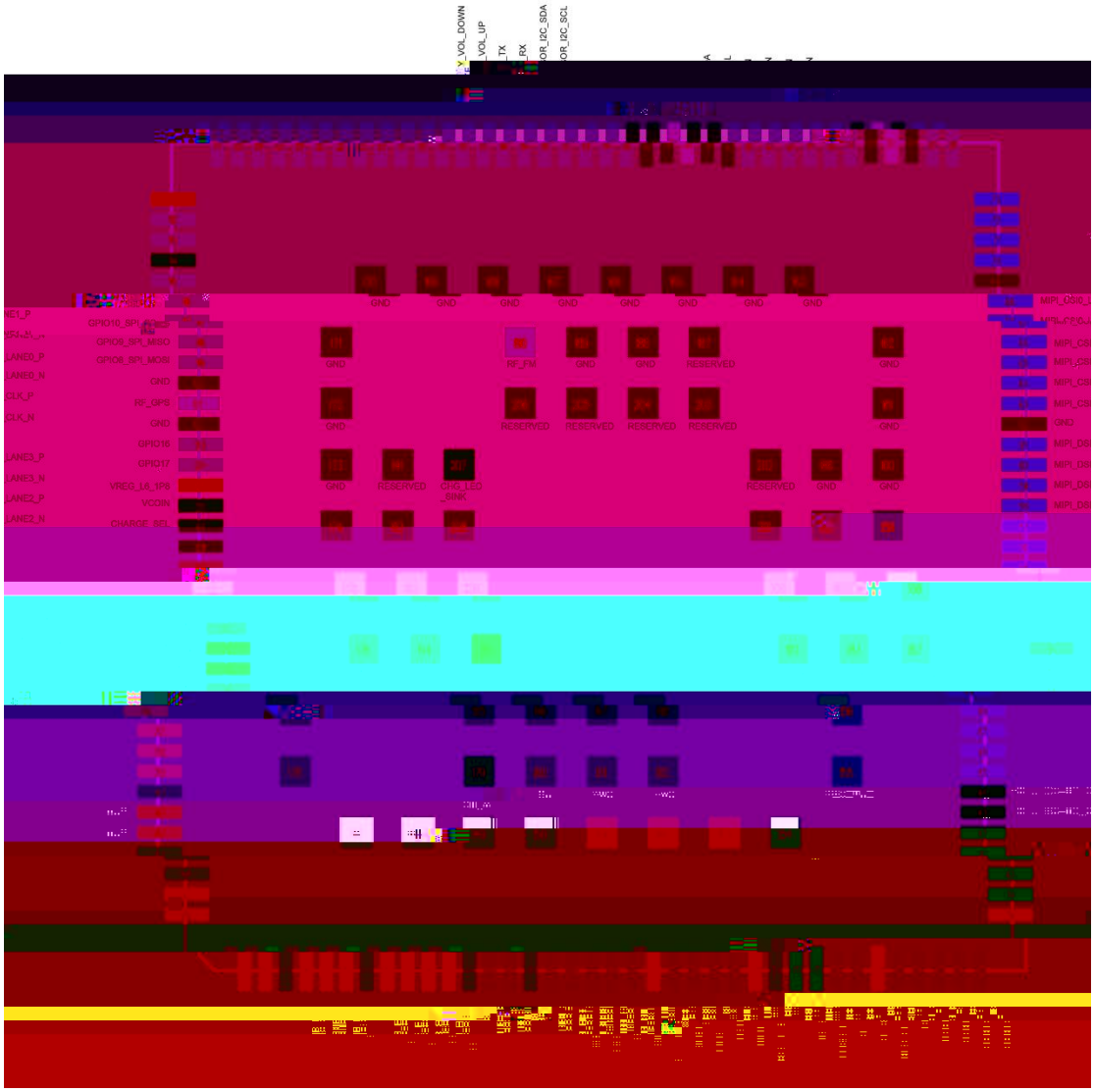
157			
159	RESERVED		RESERVED
160	GND	GND	GND
161	GND	GND	GND
162	GND	GND	GND
166	GND	GND	GND
167	GND	GND	GND
168	GND	GND	GND
169	GND	GND	GND
170	GND	GND	GND
171	GND	GND	GND
172	GND	GND	GND
173	GND	GND	GND

198	GND	GND	GND
199	RESERVED		RESERVED
200	GND	GND	GND
201	GND	GND	GND
202	RESERVED		RESERVED
203	RESERVED		RESERVED
204	RESERVED		RESERVED
205	RESERVED		RESERVED
206	RESERVED		RESERVED
207	CHG_LED_SINK		Negative Power charging indicator isink
208	GND	GND	GND
209	GND	GND	GND
210	CBL_PWR_N	DI	Cable poweron detect input

4. Q ‡ 2D 5 ´.


1.00
0.50
1.00
0.40
0.60
2.79
146
111
1.50
1.50
1.50
13.50
37
74
73
38

5.PIN 7J Ê y


VOL_DOWN
VOL_UP
TX
RX
I2C_SDA
I2C_SCL
GND
RF_FM
RESERVED
GPIO10_SPI_CS
GPIO9_SPI_MISO
GPIO8_SPI_MOSI
RF_GPS
GPIO16
GPIO17
VREG_L6_1P8
VCOIN
CHARGE_SEL
CHG_LED_SINK
MIPI_CS0
MIPI_CS1
MIPI_CS2
MIPI_CS3